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ELECTRONIC INFORMATION DISCLOSURE STATEMENT

Electronic Version v18

Stylesheet Version v18.0

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| Title of Invention | METHOD TO PRODUCE TRANSISTOR HAVING REDUCED GATE HEIGHT |
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Application Number : 10/604,912

Confirmation Number:

First Named Applicant: Heemyoung Park

Attorney Docket Number: FIS920030026US1

Art Unit: 2823

Examiner: B. Kebede

Search string: (6429084 or 6198142 or 6303450 or 6248637 or 6432754 or 20020142551 or 20020164841 or 20020115240 or 20020072176).pn

US Patent Documents

Note: Applicant is not required to submit a paper copy of cited US Patent Documents

| init | Cite.No. | Patent No. | Date | Patentee | Kind | Class | Subclass |
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| BK | 1 | 6429084 | 2002-08-06 | Park, et al. | | | |
| BK | 2 | 6198142 | 2001-03-06 | Chau, et al. | | | |
| BK | 3 | 6303450 | 2001-10-16 | Park, et al. | | | |
| BK | 4 | 6248637 | 2001-06-19 | Yu | | | |
| BK | 5 | 6432754 | 2002-08-13 | Assaderaghi, et al | | | |

US Published Applications

Note: Applicant is not required to submit a paper copy of cited US Published Applications

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| BK | 1 | 20020142551 | 2002-10-03 | Park, et al. | | | |
| BK | 2 | 20020164841 | 2002-11-07 | Assaderaghi, et al. | | | |
| BK | 3 | 20020115240 | 2002-08-22 | Assaderaghi, et al. | | | |
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Signature

Brook Kebede

4/29/2005